

**LISTING OF THE CLAIMS**

The following is a listing of the pending claims. No claim amendments have been made in response to the instant office action.

1. (withdrawn) A computer system, comprising:

a system bus;

a system configuration register for storing first endian information of the computer system;

at least one peripheral device having a base address register for storing second endian information; and

a host controller connected between the system bus and the at least one peripheral device, for comparing the first endian information with the second endian information, for byte-swapping data of the at least one peripheral device if the first endian information and the second endian information are different, and for transmitting the data of the at least one peripheral device to the system bus.

2. (withdrawn) The computer system of claim 1, wherein the second endian information is stored in the base address register as one bit.

3. (withdrawn) The computer system of claim 1, wherein the at least one peripheral device comprises:

a controller for controlling operation of the at least one peripheral device;

a memory device for storing data of the at least one peripheral device;

a memory controller for controlling the memory device; and

a target chip having address information of the at least one peripheral device.

4. (withdrawn) The computer system of claim 3, wherein the target chip is used for building the base address register.

5. (withdrawn) The computer system of claim 1, further comprising a main controller connected to the system bus and for controlling an operation of the computer system according to a value of the system configuration register.

6. (previously presented) A method of data transmission of a computer system, comprising:

reading first endian information of at least one peripheral device, wherein the first endian information is given by the value of a bit read from the at least one peripheral device;

determining whether second endian information of the computer system is identical with the first endian information of the at least one peripheral device;

byte-swapping data of the at least one peripheral device when the second endian information is different from the first endian information, and transmitting byte-swapped data to a system bus of the computer system; and

transmitting the data of the at least one peripheral device to the system bus when the second endian information is identical to the first endian information.

7. (original) The method of claim 6, wherein the first endian information of the at least one peripheral device is stored in a base address register.

8. (original) The method of claim 6, wherein the second endian information is stored in a system configuration register.

9. (withdrawn) A computer system, comprising:

a system bus;

a system configuration register for storing first endian information of the computer system;

at least one peripheral device for storing second endian information in an address register; and

a host controller for comparing the first endian information with the second endian information.

10. (withdrawn) The computer system of claim 9, wherein the host controller byte-swaps data of the at least one peripheral device if the first endian information and the second endian information are different.

11. (withdrawn) The computer system of claim 9, wherein the host controller transmits data of the at least one peripheral device to the system bus.

12. (withdrawn) The computer system of claim 9, wherein the host controller is

operatively coupled to the system bus and the at least one peripheral device.

13. (previously presented) A method of data transmission of a computer system, comprising:

reading first endian information of at least one peripheral device, wherein the first endian information is given by the value of a bit read from the at least one peripheral device;

determining whether second endian information of the computer system is identical with the first endian information of the at least one peripheral device; and

byte-swapping data of the at least one peripheral device when the second endian information is different from the first endian information.

14. (original) The method of claim 13, further comprising:

transmitting byte-swapped data of the at least one peripheral device to a system bus of the computer system.

15. (original) The method of claim 13, further comprising:

transmitting the data of the at least one peripheral device to a system bus of the computer system when the second endian information of is identical with the first endian information.

16. (previously presented) A method of data review of a computer system, comprising:

reading first endian information of at least one peripheral device; and

determining whether second endian information of the computer system is identical with the first endian information of the at least one peripheral device, wherein the determination is performed by comparing the value of a bit of the at least one peripheral device to the value of a bit of the computer system.